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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/646,413	08/21/2003	Oleg Logvinov	102362-32	9274
27388	7590	06/29/2005	EXAMINER	
NORRIS, MC LAUGHLIN & MARCUS				NGUYEN, MIKE
875 THIRD AVE				ART UNIT
18TH FLOOR				PAPER NUMBER
NEW YORK, NY 10022				2182

DATE MAILED: 06/29/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>
	10/646,413	LOGVINOV ET AL.
	Examiner Mike Nguyen	Art Unit 2182

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

#### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

- 1) Responsive to communication(s) filed on 20 January 2004.
- 2a) This action is FINAL.                    2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

- 4) Claim(s) 1-7 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) Claim(s) \_\_\_\_\_ is/are allowed.
- 6) Claim(s) 1-7 is/are rejected.
- 7) Claim(s) \_\_\_\_\_ is/are objected to.
- 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on 21 August 2003 is/are: a) accepted or b) objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) All    b) Some \* c) None of:
1. Certified copies of the priority documents have been received.
  2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

- 1) Notice of References Cited (PTO-892)
- 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date 01/20/2004.
- 4) Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_.
- 5) Notice of Informal Patent Application (PTO-152)
- 6) Other: \_\_\_\_\_.

## **DETAILED ACTION**

1. Claims 1-14 are pending for the examination.

### ***Claim Rejections - 35 USC § 102***

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

3. Claims 1-7 are rejected under 35 U.S.C. 102(e) as being anticipated by Zalitzky et al. (U.S. Pub. No. 2004/0037317 A1).

As to claim 1, Zalitzky teaches a power line communications transceiver (transceiver 34 of fig. 2) comprising:

a plurality of hardwired logic (HWL) modules (logic module 60 of fig. 2) coupled to a plurality of digital signal processors (DSPs) (subscriber equipment interface modules 72, 78, 80, 82 and 86 of fig. 2) in an architecture permitting processing of data and data signals associated with power line communications to be performed at least one of the DSPs and HWL modules ([0042-0049], and

a memory (memory 66 of fig. 2) including software code for execution by the DSP, wherein the DSP, based on execution of the software code, controls data signal exchange between or among the HWL modules and DSPs and processing of data signals received at and to be transmitted from the transceiver by at least one of the DSPs and the HWL modules ([0043]).

As to claim 2, Zalitzky teaches the power line communication transceiver as claimed in claim 1, wherein the transceiver is implemented in PLC technology ([0006]).

As to claim 3, Zalitzky teaches the power line communication transceiver as claimed in claim 1, wherein the architecture utilizes a plurality of buses for control information and data payload (figs 2 and 3 wherein module logic 60 transfers data to and from the interface modules 72-86 via separate bus).

As to claim 4, Zalitzky teaches the power line communication transceiver as claimed in claim 3, wherein one of the plurality of buses is a control bus ([0043]).

As to claim 5, Zalitzky teaches the power line communication transceiver as claimed in claim 4, wherein the architecture includes one or more functional blocks for reconfiguration through the control bus to a required function (logic module 60 of fig. 2 and [0043]).

As to claim 6, Zalitzky teaches the power line communication transceiver as claimed in claim 3, wherein the plurality of buses is a dual bus for control and data ([0042-0043]).

As to claim 7, Zalitzky teaches the power line communication transceiver as claimed in claim 6, wherein the architecture is multi-based (fig. 2).

***Conclusion***

4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

U.S. Pat. No. 6,822,946 B1 (Wallace)

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Mike Nguyen whose telephone number is 571 272-4153. The examiner can normally be reached on 8:00AM-4:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Dov Popovici can be reached on 571 272-4083. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

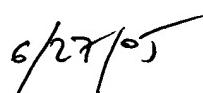
Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Mike Nguyen  
Patent Examiner  
Group Art Unit 2182

06/27/2005



KIM HUYNH  
PRIMARY EXAMINER



6/27/05